



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richmond et.al.

Assignee: ZiLOG, Inc.

Title: "Clock Controller with Clock Source Fail-Safe Logic"

Appl. No.: 10/764,391

Filing Date: January 23, 2004

Examiner: James F. Sugent

Art Unit: 2116

Docket No.: ZIL-555

February 27, 2007

Mail Stop AF  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO FINAL OFFICE ACTION**

Dear Sir:

In response to the outstanding, final office action dated December 27, 2006 ("Office Action"), Applicants respond as follows.

There are no amendments to the specification in this Response.

A listing of claims begins on page 2 of this Response.

There are no amendments to the drawings in this Response.

The **Remarks** begin on page 7 of this Response.

DO NOT  
ENTER  
3/9/2007  
JF Sugent